

Serial No.: 09/940,010

IN THE SPECIFICATION:

Please replace paragraph number [0020] with the following:

B1 [0020] During normal operation, the buffer enable bank 105 is disabled for the first test mode and enabled for normal operation by the test mode signal having the second logic state at node 118 in order that user data can be driven from nodes 95 through the buffer enable bank 105 and the input/output buffer 80 to pins 70, 126, 127, or 128.